

[ADNTA]

[User's Manual]



六联智能
SIXUNITED

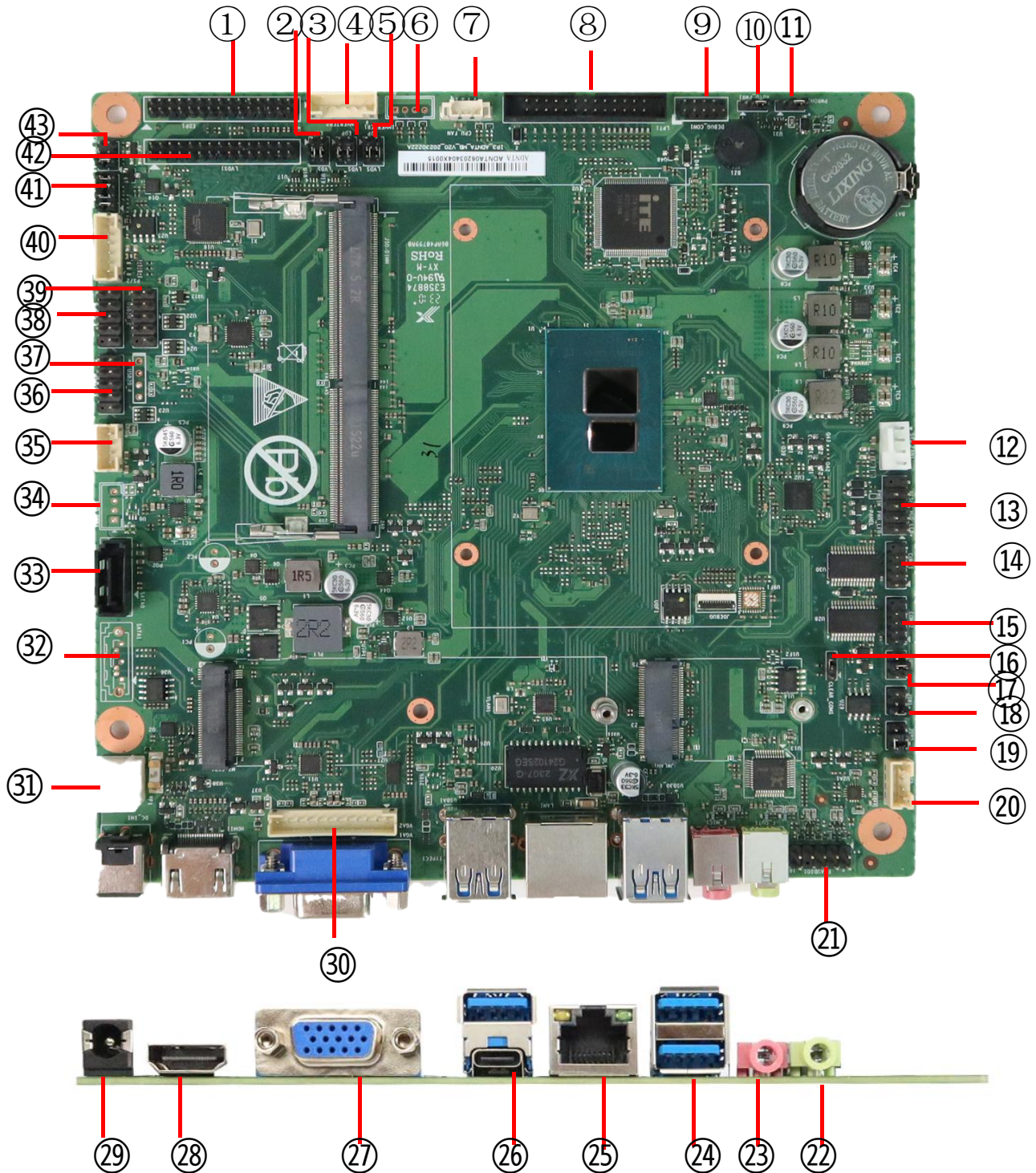
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1. Specifications

Model	ADNTA
CPU	Support Intel Alder lake N CPU ADNTA-SKU#2: N100/4C/4T/Base Freq 2.9 Ghz/TDP up to 6W ADNTA-SKU#3: N200/4C/4T/Base Freq 3.2 Ghz/TDP up to 6W ADNTA-SKU#4: i3-N300/8C/8T/Base Freq 2.6 Ghz/TDP up to 7W ADNTA-SKU#5: i3-N305/8C/8T/Base Freq 3.0 Ghz/TDP up to 15W
Memory	1*DDR4 SO-DIMM, Single Channel, Up to 16GB
Graphics	1*HDMI 1.4b, Maximum resolution 4096x2160@30Hz 1*VGA, Maximum resolution 1920x1200@60Hz LVDS,Maximum solution 1920x1080@60Hz, Share with EDP eDP,Maximum solution 4096x2160@60Hz,Share with LVDS
Storage	1*M.2 M Key 2242/2280, support SATA3.0 2*SATA(one is share with M.2 2280 slot)
LAN	1*Realtek RTL8111H 1000M LAN
Serial Port	2, COM1 RS232,COM2 RS485,when use IT 8786
USB	1*USB3.2 Gen1, 2*USB3.2 Gen2, 1*Type-C, 6*USB2.0
Rear IO	1*12V DC_JACK 1*HDMI1.4 1*VGA 1*RJ45 1*Line_out 1*Mic_in 2*USB3.2 Gen2 1*USB 3.2 Gen1 1*Type C
Front I/O	NA
Internal I/O	1*M.2 M Key 2242/2280 for SSD (SATA3.0) 1*M.2 E Key 2230 for WIFI (PCIe&USB&CNVI) 1*2x5 USB2.0 Header 1*1X4 USB 2.0 Header (4 USB2.0 can be expanded through HUB) 1*VGA 1*PS2 2*SATA 2*SATA POWER 1*F PANEL 1*F AUDIO 2*COM 1*SATA 1*SATA POWER 1*Internal Stereo Speaker 1*FAN Headers 1*LVDS 1*EDP 1*Clear COMS
Size	170mm *170mm
Temperature	Operation: -10~60°C, Storage: -40~85°C
Relative Humidity	Operation: 10~90%, Storage: 5~95%
Power Input	DC 12V 1

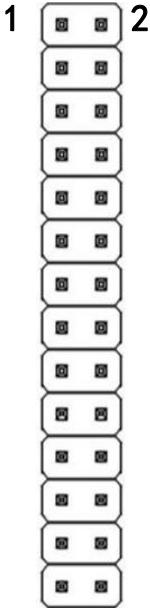
2. Locations



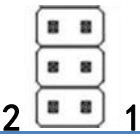
Locations	
1	EDP1
2	LCD_eDP_SEL3
3	LCD_eDP_SEL2
4	INVERTER2
5	LCD_eDP_SEL1
6	INVERTER1
7	CPU_FAN1
8	LPT1
9	DEBUG_CON1
10	AUTO_PWR1
11	PWR_ON1
12	SYS_FAN1
13	F_PANEL1
14	COMA1
15	COMA2
16	CLEAR_CMOS
17	JPW1
18	JSW1
19	JSW2
20	SPK
21	F_AUDIO1
22	F_OUT1
23	F_MIC1
24	USB30_1
25	LAN1
26	USBA1&TYPE-C1
27	VGA1
28	HDMI1
29	DC_IN1
30	VGA2
31	ATXPWR1
32	SATA1
33	SATA2
34	HDD_PWR1
35	HDD_PWR2
36	USB20_4
37	USB2.0
38	USB20_3
39	USB20_2
40	PS/2
41	LVDS_SEL1
42	LVDS1
43	LVDS_VOL_SEL1

3. Connector and Jumper Setting


1) EDP

Graphic	PIN	Define	PIN	Define
	1	BLK_PWR1	2	BLK_PWR2
	3	BLK_PWR3	4	GND1
	5	GND2	6	NC1
	7	NC2	8	NC3
	9	LCD_VCC1	10	NC4
	11	LCD_VCC2	12	PWM
	13	GND4	14	GND5
	15	VDDEN	16	AUXP_EDP
	17	EDP_HPDP	18	AUXN_EDP
	19	GND3	20	GND6
	21	EDP_P3	22	EDP_N3
	23	EDP_P2	24	EDP_N2
	25	GND8	26	GND7
	27	EDP_P1	28	EDP_N1
29	EDP_P0	30	EDP_N0	


2) LCD_EDP_SEL1

Graphic	Setting	Function
	1-3	EDP
	2-4 (Default)	
	3-5	LVDS
	4-6	

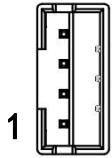
3) INVERTER2

Graphic	PIN	Define
	1	+12V
	2	+12V
	3	BKLT_ON
	4	BKLT_PWM1
	5	GND
	6	GND

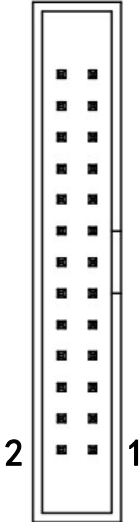
4) INVERTER1

Graphic	PIN	Define
	1	BLUP
	2	BLDN
	3	GND
	4	CH7511B_PDN

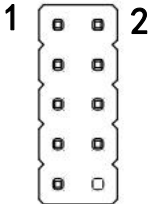
5) CPU FAN

Graphic	PIN	Define
	1	GND
	2	+V5S
	3	FAN1_TACH
	4	FAN1_PWM


6) LPT1

Graphic	PIN	Define	PIN	Define
	1	STB#	2	AFD#
	3	PD0	4	ERR#
	5	PD1	6	INT#
	7	PD2	8	SLIN#
	9	PD3	10	GND
	11	PD4	12	GND1
	13	PD5	14	GND2
	15	PD6	16	GND3
	17	PD7	18	GND4
	19	ACK#	20	GND5
	21	BUSY	22	GND6
	23	PE	24	GND7
	25	SLCT	26	GND8


7) DEBUG CON1

Graphic	PIN	Define	PIN	Define
	1	+V3.3S	2	PLTRST_C
	3	ESPICK	4	ESPI[0]
	5	ESPI_CS#	6	ESPI[1]
	7	ESPI[3]	8	ESPI[2]
	9	GND		


8) AUTO PWRON

Graphic	Setting	Function
1 	1-2	AT Mode
	2-3 (Default)	ATX Mode

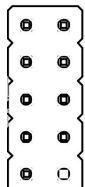
9) PWR ON

Graphic	Setting	Function
1 	1-2	AT Mode
	2-3 (Default)	ATX Mode

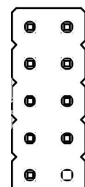
10) SYS_FAN1

Graphic	PIN	Define
1 	1	GND
	2	+12VS
	3	FAN3_TACH
	4	FAN3_PWM

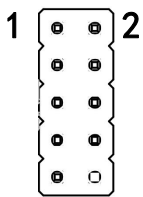
11) FPANEL

Graphic	PIN	Define	PIN	Define
1  2	1	HDLED+	2	PWRLED+
	3	HDLED-	4	GND
	5	GND	6	PWRRSW_N
	7	RERST_BTN	8	GND
	9	NA		


12) COM1

Graphic	PIN	Define	PIN	Define
1  2	1	COM1_DCD_CN /RS485 N1	2	COM1_RXD_CN /RS485 P1
	3	COM1_TXD_CN	4	COM1_DTR_CN
	5	GND	6	COM1_DSR_CN
	7	COM1_RTS_CN	8	COM1_CTS_CN
	9	COM1_RI_CN		

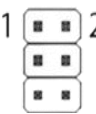
13) COM2

Graphic	PIN	Define	PIN	Define
	1	COM2_DCD_CN	2	COM2_RXD_CN
	3	COM2_TXD_CN	4	COM2_DTR_CN
	5	GND	6	COM2_DSR_CN
	7	COM2_RTS_CN	8	COM2_CTS_CN
	9	COM2_RI_CN		

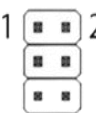
14) CLEAR CMOS

Graphic	Setting	Function
	1-2	CLEAR CMOS
	2-3 (Default)	Normal

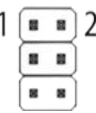
15) JSW1(COM2)

Graphic	Setting	Function
	1-2 (Default)	RS232
	3-4	RS485
	5-6	


16) JPW1(COM2)

Graphic	Setting	Function
	1-2	5V
	3-4	
	3-4 (Default)	RI
	3-4	12V
5-6		

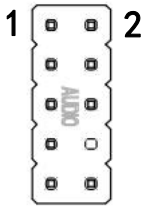
17) JSW2(COM2)

Graphic	Setting	Function
	1-3	RS232
	2-4 (Default)	
	3-5	RS485
	4-6	

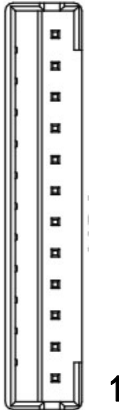
18) SPK

Graphic	PIN	Define
	1	SPKR_RN_CON
	2	SPKR_RP_CON
	3	SPKR_LN_CON
	4	SPKR_LP_CON


19) F AUDIO

Graphic	PIN	Define	PIN	Define
	1	MIC2_L	2	GND_AUD
	3	MIC3_R	4	+3.3VD
	5	LINE2-R	6	MIC2_JD
	7	GND_AUD	8	NA
	9	LINE2-L	10	LINE2-JD


20) VGA2

Graphic	PIN	Define
	1	GND
	2	VSOUT
	3	HSOUT
	4	GND
	5	VGA_R+
	6	GND
	7	VGA_G+
	8	GND
	9	VGA_B+
	10	GND
	11	VGA_DDCSDA
	12	VGA_DDCSCL

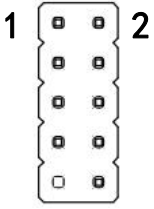
21) HDD PWR1

Graphic	PIN	Define
	1	+12V
	2	GND
	3	GND
	4	+5S

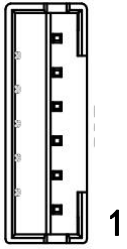
22) USB 2.0

Graphic	PIN	Define
	1	+V5P0A_USB_P4
	2	USB2_P6_DN
	3	USB2_P6_DP
	4	GND

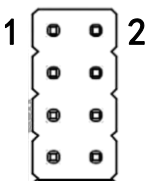
23) USB20_2

Graphic		PIN	Define	PIN	Define
	1	1	+V5P0A_USB_P3	2	+V5P0A_USB_P3
	3	3	USB2_HEADER2_D N	4	USB2_HEADER1_DN
	5	5	USB2_HEADER2_DP	6	USB2_HEADER1_DP
	7	7	GND	8	GND
	9	9	NA	10	GND

24) PS/2

Graphic		PIN	Define
	1	1	KB_CLK
	2	2	KB_DATA
	3	3	MS_CLK
	4	4	GND
	5	5	PS2_VCC
	6	6	MS_DAT

25) LVDS SEL1

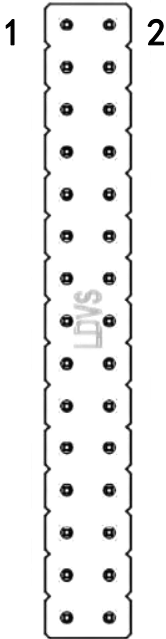
Graphic		PIN	Define	PIN	Define
	1	1	GPIO0	2	V3P3A_LCD
	3	3	GPIO1	4	V3P3A_LCD
	5	5	GPIO2	6	V3P3A_LCD
	7	7	GPIO3	8	V3P3A_LCD

LVDS resolution selection (Select by BIOS)

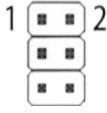
	PIN8-PIN7	PIN6-PIN5	PIN4-PIN3	PIN2-PIN1	RES
0	0	0	0	0	800*600 1channel 6Bit
1	0	0	0	1	800*600 2channel 6Bit
2	0	0	1	0	1024*600 2channel 8Bit
3	0	0	1	1	1024*600 2channel 6Bit
4	0	1	0	0	1024*768 1channel 6Bit
5	0	1	0	1	1024*768 1channel 8Bit
6	0	1	1	0	1280*800 2channel 8Bit
7	0	1	1	1	1280*800 2channel 6Bit
8	1	0	0	0	1280*1024 2channel 8Bit
9	1	0	0	1	1280*1024 1channel 6Bit
10	1	0	1	0	1366*768 1channel 8Bit
11	1	0	1	1	1366*768 2channel 8Bit
12	1	1	0	0	1680*1050 1channel 6Bit
13	1	0	1	1	1600*1200 2channel 6Bit
14	1	1	1	0	1920*1080 2channel 8Bit
15	1	1	1	1	1920*1080 1channel 6Bit Default

Note: PIN1-PIN2 connection is expressed as 1, on the contrary, it is 0. There are 16 cases in total.

26) LVDS

Graphic	PIN	Define	PIN	Define
	1	VDD1	2	VDD2
	3	VDD3	4	GND1
	5	GND2	6	GND3
	7	A0N	8	A0P
	9	A1N	10	A1P
	11	A2N	12	A2P
	13	GND4	14	GND5
	15	ACKN	16	ACKP
	17	A3N	18	A3P
	19	B0N	20	B0P
	21	B1N	22	B1P
	23	B2N	24	B2P
	25	GND6	26	GND7
	27	BCKN	28	BCKP
	29	B3N	30	B3P

27) LCD VOL SEL

Graphic	Setting	Function
	1-2 (Default)	LCD_VDD 3.3V
	3-4	LCD_VDD 5V
	5-6	LCD_VDD 12V